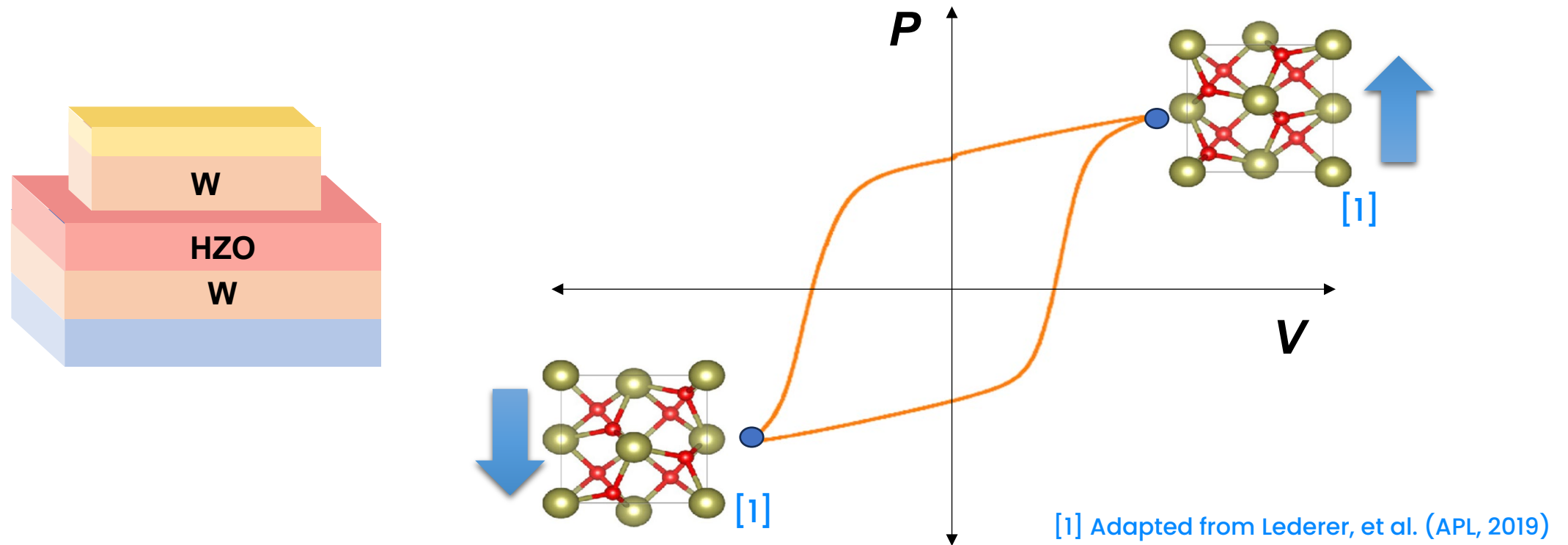


Ferroelectric Memory Devices for AI Hardware

Tyra Espedal, UROP Spring 2025
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